

IET Circuits, Devices & Systems Call for Papers

SPECIAL ISSUE ON: Thermal, Power and Timing Modeling, Design and Simulation

With the advent of new technologies and scaling design parameters, thermal and power issues have emerged as new key design factors. These technologies also exhibit restrictive timing constraints usually hard to meet as the clock frequency keeps rising. This effect is even more notorious in current embedded systems running complex processing applications, where the portability (many times attached to the user) and battery-operation requires a high performance with low energy and thermal requirements.

Designers and researchers have agreed on the need of searching for efficient tools and design methodologies that address these issues. The development of accurate models, fast estimation tools, and innovative IC and systems will be the key for the success on future achievements of the industry.

This special issue is intended to address recent advances and future research directions in all the areas related to Thermal, Power and Timing Modeling, Design and Simulation. These include:

- Timing and performance analysis tools.
- Power estimation and optimization.
- Low power architectures and system level techniques.
- Special power-related topics; Green Computing.
- Thermal-aware design and thermal estimation.
- Power and timing in specific technologies: reconfigurable architectures, caches and memory devices.
- Thermal effects on system reliability.

All papers must be submitted through the journal's Manuscript Central system:
<http://mc.manuscriptcentral.com/iet-cds>

Proposed publication schedule:

Deadline for submission of papers:
November 30th 2011

Authors to receive a 1st decision by:
February 28th 2012

Final notification of acceptance:
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