

# Call for Papers IET Computers & Digital Techniques

## Special Issue: Hardware Security



Hardware security has become a key requirement and the Achille's heel of a wide range of electronic systems, from small RFID tags to cars and entire embedded infrastructures. Its significance will further grow with the proliferation of new technologies and continued integration of information technology with the environment within the cyberphysical system paradigm.

**This Special Issue will present recent results in the field of hardware security. Topics of interest include but are not limited to:**

- Design of secure functions in application-specific and reconfigurable hardware
- Secure hardware primitives (physically unclonable functions, random number generators)
- Side-channel analysis techniques and countermeasures
- Fault attacks, detection and protection methods
- Hardware Trojans and trustworthy manufacturing
- Manufacturing test of secure devices
- Anti-counterfeiting and IP protection for semiconductor devices
- Methodologies, tools and metrics for hardware security
- Formal methods for secure hardware
- Applications of secure hardware in domains such as automotive, aerospace or industrial automation
- Case studies

All submissions are subject to the journal's peer-review procedures. The authors should follow the journal's Author guide at <http://digital-library.theiet.org/journals/author-guide> when preparing papers for submission to the Special Issue.

### Important dates:

**Submission deadline:**  
28 February 2014

**Publication Date:**  
November 2014

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All papers must be submitted through the journal's Manuscript Central system:  
<http://mc.manuscriptcentral.com/iet-cdt>

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